ROM:A0800CE0 CODE16

ROM:A0800CE0

ROM:A0800CE0 ; =============== S U B R O U T I N E =======================================

ROM:A0800CE0

ROM:A0800CE0

ROM:A0800CE0 sub\_A0800CE0 ; CODE XREF: ROM:A08C028Ep

ROM:A0800CE0 ; DATA XREF: ROM:A08C028Co ...

ROM:A0800CE0 FF B5 PUSH {R0-R7,LR}

ROM:A0800CE2 02 1C MOVS R2, R0

ROM:A0800CE4 02 21 MOVS R1, #2

ROM:A0800CE6 62 20 MOVS R0, #0x62

ROM:A0800CE8 23 DF SVC 0x23

ROM:A0800CEA 00 F0 18 E8 BLX loc\_A0800D1C

ROM:A0800CEE FF BC POP {R0-R7}

ROM:A0800CF0 01 BC POP {R0}

ROM:A0800CF2 00 1D ADDS R0, R0, #4

ROM:A0800CF4 00 47 BX R0

ROM:A0800CF4 ; End of function sub\_A0800CE0

ROM:A0800CF4

ROM:A0800CF4 ; ---------------------------------------------------------------------------

ROM:A0800CF6 00 DCB 0

ROM:A0800CF7 00 DCB 0

ROM:A0800CF8 CODE32

ROM:A0800CF8 FF FF FF 64 dword\_A0800CF8 DCD 0x64FFFFFF ; DATA XREF: ROM:off\_A0800D00o

ROM:A0800CF8 ; ROM:loc\_A0800D1Co

ROM:A0800CFC 00 00 00 64 dword\_A0800CFC DCD 0x64000000 ; DATA XREF: ROM:off\_A0800D04o

ROM:A0800CFC ; ROM:A0800D2Co

ROM:A0800D00 F8 0C 80 A0 off\_A0800D00 DCD dword\_A0800CF8 ; DATA XREF: ROM:loc\_A0800D1Cr

ROM:A0800D04 FC 0C 80 A0 off\_A0800D04 DCD dword\_A0800CFC ; DATA XREF: ROM:A0800D2Cr

ROM:A0800D08 67 66 66 66 dword\_A0800D08 DCD 0x66666667 ; DATA XREF: ROM:A0800D90r

ROM:A0800D08 ; ROM:A0800DC0r ...

ROM:A0800D0C 09 00 00 00 dword\_A0800D0C DCD 9 ; DATA XREF: ROM:A0800DB8r

ROM:A0800D0C ; ROM:A0800DF0r ...

ROM:A0800D10 3E 00 00 00 dword\_A0800D10 DCD 0x3E ; DATA XREF: ROM:A0800DA8r

ROM:A0800D10 ; ROM:A0800DE4r ...

ROM:A0800D14 16 00 00 00 dword\_A0800D14 DCD 0x16 ; DATA XREF: ROM:A0800EACr

ROM:A0800D14 ; ROM:A0800ED4r ...

ROM:A0800D18 0C 00 00 00 dword\_A0800D18 DCD 0xC ; DATA XREF: ROM:A0800EA8r

ROM:A0800D18 ; ROM:A0800ED0r ...

ROM:A0800D1C ; ---------------------------------------------------------------------------

ROM:A0800D1C

ROM:A0800D1C loc\_A0800D1C ; CODE XREF: sub\_A0800CE0+Ap

ROM:A0800D1C 24 10 1F E5 LDR R1, =dword\_A0800CF8

ROM:A0800D20 00 40 2D E9 STMFD SP!, {LR}

ROM:A0800D24 18 D0 4D E2 SUB SP, SP, #0x18

ROM:A0800D28 00 20 91 E5 LDR R2, [R1]

ROM:A0800D2C 30 10 1F E5 LDR R1, =dword\_A0800CFC

ROM:A0800D30 0D 00 A0 E1 MOV R0, SP

ROM:A0800D34 00 20 80 E5 STR R2, [R0]

ROM:A0800D38 00 20 91 E5 LDR R2, [R1]

ROM:A0800D3C 04 00 8D E2 ADD R0, SP, #4

ROM:A0800D40 00 20 80 E5 STR R2, [R0]

ROM:A0800D44 08 10 8D E2 ADD R1, SP, #8

ROM:A0800D48 10 00 8D E2 ADD R0, SP, #0x10

ROM:A0800D4C B4 00 00 EF SVC 0xB4

ROM:A0800D50 0D 00 A0 E1 MOV R0, SP

ROM:A0800D54 01 00 2D E9 STMFD SP!, {R0}

ROM:A0800D58 01 00 2D E9 STMFD SP!, {R0}

ROM:A0800D5C 7F 30 A0 E3 MOV R3, #0x7F

ROM:A0800D60 00 00 A0 E1 NOP

ROM:A0800D64 7F 20 A0 E3 MOV R2, #0x7F

ROM:A0800D68 00 00 A0 E3 MOV R0, #0

ROM:A0800D6C 01 00 2D E9 STMFD SP!, {R0}

ROM:A0800D70 0F 10 A0 E3 MOV R1, #0xF

ROM:A0800D74 00 00 A0 E3 MOV R0, #0

ROM:A0800D78 B4 01 00 EF SVC 0x1B4

ROM:A0800D7C 03 30 A0 E3 MOV R3, #3

ROM:A0800D80 7F 20 A0 E3 MOV R2, #0x7F

ROM:A0800D84 00 10 A0 E3 MOV R1, #0

ROM:A0800D88 00 00 A0 E3 MOV R0, #0

ROM:A0800D8C B4 01 00 EF SVC 0x1B4

ROM:A0800D90 90 10 1F E5 LDR R1, =0x66666667

ROM:A0800D94 0C 00 8D E2 ADD R0, SP, #0xC

ROM:A0800D98 01 00 2D E9 STMFD SP!, {R0}

ROM:A0800D9C 18 00 DD E5 LDRB R0, [SP,#0x18]

ROM:A0800DA0 14 30 8D E2 ADD R3, SP, #0x14

ROM:A0800DA4 91 20 CC E0 SMULL R2, R12, R1, R0

ROM:A0800DA8 A0 10 1F E5 LDR R1, =0x3E **; 0i62 ; y clock**

ROM:A0800DAC 4C C1 A0 E1 MOV R12, R12,ASR#2

ROM:A0800DB0 A0 0F 8C E0 ADD R0, R12, R0,LSR#31

ROM:A0800DB4 B7 00 00 FA BLX sub\_A0801098

ROM:A0800DB8 B4 00 1F E5 LDR R0, =9 **; 0i9 ; x clock**

ROM:A0800DBC 26 00 00 EF SVC 0x26

ROM:A0800DC0 C0 10 1F E5 LDR R1, =0x66666667

ROM:A0800DC4 10 00 8D E2 ADD R0, SP, #0x10

ROM:A0800DC8 01 00 2D E9 STMFD SP!, {R0}

ROM:A0800DCC 1C 00 DD E5 LDRB R0, [SP,#0x1C]

ROM:A0800DD0 18 30 8D E2 ADD R3, SP, #0x18

ROM:A0800DD4 90 21 CC E0 SMULL R2, R12, R0, R1

ROM:A0800DD8 0A 20 A0 E3 MOV R2, #0xA

ROM:A0800DDC 4C C1 A0 E1 MOV R12, R12,ASR#2

ROM:A0800DE0 92 0C 0C E0 MUL R12, R2, R12

ROM:A0800DE4 DC 10 1F E5 LDR R1, =0x3E

ROM:A0800DE8 0C 00 40 E0 SUB R0, R0, R12

ROM:A0800DEC A9 00 00 FA BLX sub\_A0801098

ROM:A0800DF0 EC 00 1F E5 LDR R0, =9

ROM:A0800DF4 1B 00 80 E2 ADD R0, R0, #0x1B **; 18 is width of digit picture, 18/2 will be space between digits, then 18+18/2=27=0x1B**

ROM:A0800DF8 26 00 00 EF SVC 0x26

ROM:A0800DFC 14 00 8D E2 ADD R0, SP, #0x14

ROM:A0800E00 01 00 2D E9 STMFD SP!, {R0}

ROM:A0800E04 1C 30 8D E2 ADD R3, SP, #0x1C

ROM:A0800E08 52 20 A0 E3+ MOV R2, 0x452 **; ":" picture**

ROM:A0800E10 08 11 1F E5 LDR R1, =0x3E

ROM:A0800E14 10 01 1F E5 LDR R0, =9

ROM:A0800E18 36 00 80 E2 ADD R0, R0, #0x36 **; 0x1B\*2=0x36 X position of ":" picture**

ROM:A0800E1C 26 00 00 EF SVC 0x26

ROM:A0800E20 20 11 1F E5 LDR R1, =0x66666667

ROM:A0800E24 18 00 8D E2 ADD R0, SP, #0x18

ROM:A0800E28 01 00 2D E9 STMFD SP!, {R0}

ROM:A0800E2C 25 00 DD E5 LDRB R0, [SP,#0x25]

ROM:A0800E30 20 30 8D E2 ADD R3, SP, #0x20

ROM:A0800E34 91 20 CC E0 SMULL R2, R12, R1, R0

ROM:A0800E38 30 11 1F E5 LDR R1, =0x3E

ROM:A0800E3C 4C C1 A0 E1 MOV R12, R12,ASR#2

ROM:A0800E40 A0 0F 8C E0 ADD R0, R12, R0,LSR#31

ROM:A0800E44 93 00 00 FA BLX sub\_A0801098

ROM:A0800E48 44 01 1F E5 LDR R0, =9

ROM:A0800E4C 45 00 80 E2 ADD R0, R0, #0x45 **; 6 is width of ":" picture, 6+18/2=15=0xF, 0x36+0xF=0x45 position of first digit after ":"**

ROM:A0800E50 26 00 00 EF SVC 0x26

ROM:A0800E54 54 11 1F E5 LDR R1, =0x66666667

ROM:A0800E58 1C 00 8D E2 ADD R0, SP, #0x1C

ROM:A0800E5C 01 00 2D E9 STMFD SP!, {R0}

ROM:A0800E60 29 00 DD E5 LDRB R0, [SP,#0x29]

ROM:A0800E64 24 30 8D E2 ADD R3, SP, #0x24

ROM:A0800E68 90 21 CC E0 SMULL R2, R12, R0, R1

ROM:A0800E6C 0A 20 A0 E3 MOV R2, #0xA

ROM:A0800E70 4C C1 A0 E1 MOV R12, R12,ASR#2

ROM:A0800E74 92 0C 0C E0 MUL R12, R2, R12

ROM:A0800E78 70 11 1F E5 LDR R1, =0x3E

ROM:A0800E7C 0C 00 40 E0 SUB R0, R0, R12

ROM:A0800E80 84 00 00 FA BLX sub\_A0801098

ROM:A0800E84 80 01 1F E5 LDR R0, =9

ROM:A0800E88 60 00 80 E2 ADD R0, R0, #0x60 **; 0x45+0x1B=0x60 position of second digit after ":"**

ROM:A0800E8C 26 00 00 EF SVC 0x26

ROM:A0800E90 C8 80 00 EF SVC 0x80C8

ROM:A0800E94 00 00 D0 E5 LDRB R0, [R0]

ROM:A0800E98 24 30 8D E2 ADD R3, SP, #0x24

ROM:A0800E9C 81 00 00 EF SVC 0x81

ROM:A0800EA0 00 70 A0 E1 MOV R7, R0

ROM:A0800EA4 7C 00 00 FB BLX sub\_A080109E

ROM:A0800EA8 98 11 1F E5 LDR R1, =0xC **; 0i12 ; y akku**

ROM:A0800EAC A0 01 1F E5 LDR R0, =0x16 **; 0i22 ; x akku**

ROM:A0800EB0 36 00 80 E2 ADD R0, R0, #0x36 **; 0x1B\*2=0x36 position of last digit akku picture**

ROM:A0800EB4 26 00 00 EF SVC 0x26

ROM:A0800EB8 09 00 57 E3 CMP R7, #9

ROM:A0800EBC 08 00 00 CA BGT loc\_A0800EE4

ROM:A0800EC0 00 00 57 E3 CMP R7, #0

ROM:A0800EC4 14 00 00 0A BEQ loc\_A0800F1C

ROM:A0800EC8 24 30 8D E2 ADD R3, SP, #0x24

ROM:A0800ECC 74 00 00 FA BLX sub\_A08010A4

ROM:A0800ED0 C0 11 1F E5 LDR R1, =0xC

ROM:A0800ED4 C8 01 1F E5 LDR R0, =0x16

ROM:A0800ED8 1B 00 80 E2 ADD R0, R0, #0x1B **; 0x1B**

ROM:A0800EDC 26 00 00 EF SVC 0x26

ROM:A0800EE0 0D 00 00 EA B loc\_A0800F1C

ROM:A0800EE4 ; ---------------------------------------------------------------------------

ROM:A0800EE4

ROM:A0800EE4 loc\_A0800EE4 ; CODE XREF: ROM:A0800EBCj

ROM:A0800EE4 07 00 A0 E1 MOV R0, R7

ROM:A0800EE8 81 00 00 EF SVC 0x81

ROM:A0800EEC 00 70 A0 E1 MOV R7, R0

ROM:A0800EF0 24 30 8D E2 ADD R3, SP, #0x24

ROM:A0800EF4 68 00 00 FB BLX sub\_A080109E

ROM:A0800EF8 E8 11 1F E5 LDR R1, =0xC

ROM:A0800EFC F0 01 1F E5 LDR R0, =0x16

ROM:A0800F00 1B 00 80 E2 ADD R0, R0, #0x1B **; 0x1B**

ROM:A0800F04 26 00 00 EF SVC 0x26

ROM:A0800F08 24 30 8D E2 ADD R3, SP, #0x24

ROM:A0800F0C 64 00 00 FA BLX sub\_A08010A4

ROM:A0800F10 00 12 1F E5 LDR R1, =0xC

ROM:A0800F14 08 02 1F E5 LDR R0, =0x16

ROM:A0800F18 26 00 00 EF SVC 0x26

ROM:A0800F1C

ROM:A0800F1C loc\_A0800F1C ; CODE XREF: ROM:A0800EC4j

ROM:A0800F1C ; ROM:A0800EE0j

ROM:A0800F1C 24 30 8D E2 ADD R3, SP, #0x24

ROM:A0800F20 90 21 9F E5 LDR R2, =0x123 **; persent picture number**

ROM:A0800F24 14 12 1F E5 LDR R1, =0xC **; 0i12 ; y akku**

ROM:A0800F28 10 10 81 E2 ADD R1, R1, #0x10 **; height of persent picture Y=20, height of digit picture Y=36. 36-20=16=0x10**

ROM:A0800F2C 20 02 1F E5 LDR R0, =0x16

ROM:A0800F30 3C 00 80 E2 ADD R0, R0, #0x3C

ROM:A0800F34 26 00 00 EF SVC 0x26

ROM:A0800F38 20 D0 8D E2 ADD SP, SP, #0x20

ROM:A0800F3C 03 00 00 EA B loc\_A0800F50

ROM:A0800F3C ; ---------------------------------------------------------------------------

ROM:A0800F40 09 00 00 00 dword\_A0800F40 DCD 9 ; DATA XREF: ROM:A0800F94r

ROM:A0800F40 ; ROM:A0800FCCr ...

ROM:A0800F44 84 00 00 00 dword\_A0800F44 DCD 0x84 ; DATA XREF: ROM:A0800F84r

ROM:A0800F44 ; ROM:A0800FC0r ...

ROM:A0800F48 36 00 00 00 dword\_A0800F48 DCD 0x36 ; DATA XREF: ROM:A0801088r

ROM:A0800F4C 50 00 00 00 dword\_A0800F4C DCD 0x50 ; DATA XREF: ROM:A0801084r

ROM:A0800F50 ; ---------------------------------------------------------------------------

ROM:A0800F50

ROM:A0800F50 loc\_A0800F50 ; CODE XREF: ROM:A0800F3Cj

ROM:A0800F50 23 EB 0D FB BLX sub\_A0B7BBE6

ROM:A0800F54 08 10 8D E2 ADD R1, SP, #8

ROM:A0800F58 CF 80 00 EF SVC 0x80CF

ROM:A0800F5C 04 00 40 E2 SUB R0, R0, #4

ROM:A0800F60 00 00 90 E5 LDR R0, [R0]

ROM:A0800F64 00 00 81 E5 STR R0, [R1]

ROM:A0800F68 0C D0 4D E2 SUB SP, SP, #0xC

ROM:A0800F6C 6C 12 1F E5 LDR R1, =0x66666667

ROM:A0800F70 0C 00 8D E2 ADD R0, SP, #0xC

ROM:A0800F74 01 00 2D E9 STMFD SP!, {R0}

ROM:A0800F78 18 00 DD E5 LDRB R0, [SP,#0x18]

ROM:A0800F7C 14 30 8D E2 ADD R3, SP, #0x14

ROM:A0800F80 91 20 CC E0 SMULL R2, R12, R1, R0

ROM:A0800F84 48 10 1F E5 LDR R1, =0x84

ROM:A0800F88 4C C1 A0 E1 MOV R12, R12,ASR#2

ROM:A0800F8C A0 0F 8C E0 ADD R0, R12, R0,LSR#31

ROM:A0800F90 40 00 00 FA BLX sub\_A0801098

ROM:A0800F94 5C 00 1F E5 LDR R0, =9

ROM:A0800F98 26 00 00 EF SVC 0x26

ROM:A0800F9C 9C 12 1F E5 LDR R1, =0x66666667

ROM:A0800FA0 10 00 8D E2 ADD R0, SP, #0x10

ROM:A0800FA4 01 00 2D E9 STMFD SP!, {R0}

ROM:A0800FA8 1C 00 DD E5 LDRB R0, [SP,#0x1C]

ROM:A0800FAC 18 30 8D E2 ADD R3, SP, #0x18

ROM:A0800FB0 90 21 CC E0 SMULL R2, R12, R0, R1

ROM:A0800FB4 0A 20 A0 E3 MOV R2, #0xA

ROM:A0800FB8 4C C1 A0 E1 MOV R12, R12,ASR#2

ROM:A0800FBC 92 0C 0C E0 MUL R12, R2, R12

ROM:A0800FC0 84 10 1F E5 LDR R1, =0x84

ROM:A0800FC4 0C 00 40 E0 SUB R0, R0, R12

ROM:A0800FC8 32 00 00 FA BLX sub\_A0801098

ROM:A0800FCC 94 00 1F E5 LDR R0, =9 **; 0i9 ; x alarm clock**

ROM:A0800FD0 1B 00 80 E2 ADD R0, R0, #0x1B **; 18 is width of digit picture, 18/2 will be space between digits, then 18+18/2=27=0x1B**

ROM:A0800FD4 26 00 00 EF SVC 0x26

ROM:A0800FD8 14 00 8D E2 ADD R0, SP, #0x14

ROM:A0800FDC 01 00 2D E9 STMFD SP!, {R0}

ROM:A0800FE0 1C 30 8D E2 ADD R3, SP, #0x1C

ROM:A0800FE4 52 20 A0 E3+ MOV R2, 0x452 **; ":" picture**

ROM:A0800FEC B0 10 1F E5 LDR R1, =0x84

ROM:A0800FF0 B8 00 1F E5 LDR R0, =9

ROM:A0800FF4 36 00 80 E2 ADD R0, R0, #0x36 **; 0x1B\*2=0x36 X position of ":" picture**

ROM:A0800FF8 26 00 00 EF SVC 0x26

ROM:A0800FFC FC 12 1F E5 LDR R1, =0x66666667

ROM:A0801000 18 00 8D E2 ADD R0, SP, #0x18

ROM:A0801004 01 00 2D E9 STMFD SP!, {R0}

ROM:A0801008 25 00 DD E5 LDRB R0, [SP,#0x25]

ROM:A080100C 20 30 8D E2 ADD R3, SP, #0x20

ROM:A0801010 91 20 CC E0 SMULL R2, R12, R1, R0

ROM:A0801014 D8 10 1F E5 LDR R1, =0x84 **; 0i132 ; y alarm clock**

ROM:A0801018 4C C1 A0 E1 MOV R12, R12,ASR#2

ROM:A080101C A0 0F 8C E0 ADD R0, R12, R0,LSR#31

ROM:A0801020 1C 00 00 FA BLX sub\_A0801098

ROM:A0801024 EC 00 1F E5 LDR R0, =9

ROM:A0801028 45 00 80 E2 ADD R0, R0, #0x45 **; 6 is width of ":" picture, 6+18/2=15=0xF, 0x36+0xF=0x45 position of first digit after ":"**

ROM:A080102C 26 00 00 EF SVC 0x26

ROM:A0801030 30 13 1F E5 LDR R1, =0x66666667

ROM:A0801034 1C 00 8D E2 ADD R0, SP, #0x1C

ROM:A0801038 01 00 2D E9 STMFD SP!, {R0}

ROM:A080103C 29 00 DD E5 LDRB R0, [SP,#0x29]

ROM:A0801040 24 30 8D E2 ADD R3, SP, #0x24

ROM:A0801044 90 21 CC E0 SMULL R2, R12, R0, R1

ROM:A0801048 0A 20 A0 E3 MOV R2, #0xA

ROM:A080104C 4C C1 A0 E1 MOV R12, R12,ASR#2

ROM:A0801050 92 0C 0C E0 MUL R12, R2, R12

ROM:A0801054 18 11 1F E5 LDR R1, =0x84

ROM:A0801058 0C 00 40 E0 SUB R0, R0, R12

ROM:A080105C 0D 00 00 FA BLX sub\_A0801098

ROM:A0801060 28 01 1F E5 LDR R0, =9

ROM:A0801064 60 00 80 E2 ADD R0, R0, #0x60 **; 0x45+0x1B=0x60 position of second digit after ":"**

ROM:A0801068 26 00 00 EF SVC 0x26

ROM:A080106C 24 30 8D E2 ADD R3, SP, #0x24

ROM:A0801070 CF 80 00 EF SVC 0x80CF

ROM:A0801074 00 00 D0 E5 LDRB R0, [R0]

ROM:A0801078 F0 00 50 E3 CMP R0, #0xF0

ROM:A080107C 2C 20 9F 05 LDREQ R2, =0x43D

ROM:A0801080 2C 20 9F 15 LDRNE R2, =0x43C

ROM:A0801084 40 11 1F E5 LDR R1, =0x50

ROM:A0801088 48 01 1F E5 LDR R0, =0x36

ROM:A080108C 26 00 00 EF SVC 0x26

ROM:A0801090 38 D0 8D E2 ADD SP, SP, #0x38

ROM:A0801094 00 80 BD E8 LDMFD SP!, {PC}

ROM:A0801098 CODE16

ROM:A0801098

ROM:A0801098 ; =============== S U B R O U T I N E =======================================

ROM:A0801098

ROM:A0801098

ROM:A0801098 sub\_A0801098 ; CODE XREF: ROM:A0800DB4p

ROM:A0801098 ; ROM:A0800DECp ...

ROM:A0801098 04 4A LDR R2, =0x447

ROM:A080109A 82 18 ADDS R2, R0, R2

ROM:A080109C 70 47 BX LR

ROM:A080109C ; End of function sub\_A0801098

ROM:A080109C

ROM:A080109E

ROM:A080109E ; =============== S U B R O U T I N E =======================================

ROM:A080109E

ROM:A080109E

ROM:A080109E sub\_A080109E ; CODE XREF: ROM:A0800EA4p

ROM:A080109E ; ROM:A0800EF4p

ROM:A080109E 03 4A LDR R2, =0x447

ROM:A08010A0 8A 18 ADDS R2, R1, R2

ROM:A08010A2 70 47 BX LR

ROM:A08010A2 ; End of function sub\_A080109E

ROM:A08010A2

ROM:A08010A4

ROM:A08010A4 ; =============== S U B R O U T I N E =======================================

ROM:A08010A4

ROM:A08010A4

ROM:A08010A4 sub\_A08010A4 ; CODE XREF: ROM:A0800ECCp

ROM:A08010A4 ; ROM:A0800F0Cp

ROM:A08010A4 01 4A LDR R2, =0x447

ROM:A08010A6 BA 18 ADDS R2, R7, R2

ROM:A08010A8 70 47 BX LR

ROM:A08010A8 ; End of function sub\_A08010A4

ROM:A08010A8

ROM:A08010AA ; ---------------------------------------------------------------------------

ROM:A08010AA C0 46 NOP

ROM:A08010AA ; ---------------------------------------------------------------------------

ROM:A08010AC 47 04 00 00 dword\_A08010AC DCD 0x447 ; DATA XREF: sub\_A0801098r

ROM:A08010AC ; sub\_A080109Er ...

ROM:A08010B0 3D 04 00 00 dword\_A08010B0 DCD 0x43D ; DATA XREF: ROM:A080107Cr

ROM:A08010B4 3C 04 00 00 dword\_A08010B4 DCD 0x43C ; DATA XREF: ROM:A0801080r

ROM:A08010B8 23 01 00 00 dword\_A08010B8 DCD 0x123 ; DATA XREF: ROM:A0800F20r